



Description

- Up to 11.1Gbps Data Links
- Maximum link length of 220M MMF
- Power dissipation < 1W
- 1310nm FP transmitter, PIN photo-detector
- Metal enclosure, for lower EMI
- 2-wire interface with integrated Digital

Diagnostic monitoring

- Hot-pluggable SFP+ footprint
- Specifications compliant with SFF 8472
- Compliant with SFP+ MSA with LC connector
- Single 3.3V power supply
- Case operating temperature range:0°C to 70°C

Applications

10GBASE-LRM

Standart

- Compliant to SFP+ SFF-8431
- Compliant to 802.3aq 10GBASE-LRM.
- RoHS Compliant.

Ordering Information

PART NUMBER	INPUT/OUTPUT	SIGNAL DETECT	VOLTAGE	TEMPERATURE
CL-SFP+LRM_220	AC/AC	TTL	3.3V/5V	- 0°C to 70 °C
CL-SFP+LRM_220i	AC/AC	TTL	3.3V/5V	-10°C to 85 °C



L. Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Storage Temperature	Ts	-40	-	85	°C	
Relative Humidity	RH	5	-	95	%	
Power Supply Voltage	VCC	-0.3	-	4	V	
Signal Input Voltage		Vcc-0.3	-	Vcc+0.3	V	

II. Recommended Operating Conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Case Operating Temperature	Tcase	0	-	70	°C	Without air flow
Power Supply Voltage	VCC	3.14	3.3	3.47	V	
Power Supply Current	ICC	-	4	300	mA	
Data Rate	BR		10.3125		Gbps	
Transmission Distance	TD		-	220	m	
Coupled fiber	N		Multi mode f	iber		MMF

III . Optical Characteristics

Parameter	Symbol	Min	Тур	Max	Unit	NOTE
Transmitter						
Average Launch Power	Роит	-6.5		0.5	dBm	1
Optical Wavelength	λ	1290	1310	1330	nm	
Optical Extinction Ratio	ER	3.5			dB	
Side Mode Supression Ratio	SMSR	30			dB	
RIN	RIN			-128	dB/Hz	
Output Eye Mask	Compliant with IEEE 802.3aq					
Receiver						
Receiver Sensitivity@10.3125G	Sen			-10	dBm	2
Input Saturation Power (Overload)	Psat	1.5			dBm	
Wavelength Range	λC	1260		1350	nm	
LOS De -Assert	LOSD			-14	dBm	
LOS Assert	LOSA	-30			dBm	
LOS Hysteresis		0.5			dB	

Notes:

- 1. Average power figures are informative only, per IEEE802.3aq
- 2. Conditions of stressed receiver tests per IEEE802.3aq.



IV. Electrical Characteristics

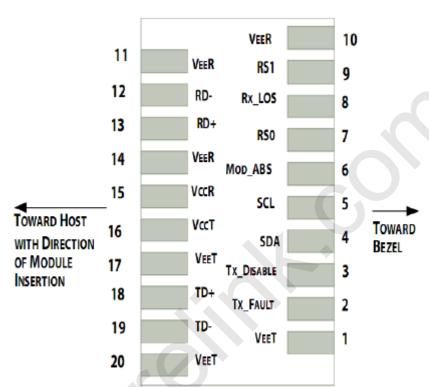
Parameter	Symbol	Min	Тур	Max	Unit	NOTE
Supply Voltage	Vcc	3.14	3.3	3.46	V	
Supply Current	Icc			300	mA	
Transmitter						
Input differential impedance	Rin		100		Ω	1
Single ended data input swing	Vin,pp	180		700	mV	
Transmit Disable Voltage	VD	Vcc-1.3		Vcc	V	
Transmit Enable Voltage	VEN	Vee		Vee+ 0.8	V	2
Transmit Disable Assert Time				10	us	
Receiver						
Differential data output swing	Vout,pp	300	*	850	mV	3
Data output rise time	tr	30			ps	4
Data output fall time	tf	30			ps	4
LOS Fault	VLOS fault	Vcc-1.3		VccHOST	V	5
LOS Normal	VLOS norm	Vee		Vee+0.8	V	5
Power Supply Rejection	PSR	100			mVpp	6

Notes:

- 1. Connected directly to TX data input pins. AC coupled thereafter.
- 2. Or open circuit.
- 3. Into 100 ohms differential termination.
- 4. These are unfiltered 20-80% values
- 5. Loss Of Signal is LVTTL. Logic 0 indicates normal operation; logic 1 indicates no signal detected.
 - 6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.



V. Pin Descriptions



Pin out of Connector Block on Host Board



Pin	Symbol	Name/Description	
1	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1
2	T _{FAULT}	Transmitter Fault.	2
3	T _{DIS}	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line	4
5	SCL	2-wire Serial Interface Clock Line	4
6	MOD_ABS	Module Absent. Grounded within the module	4
7	RS0	Rate Select 0	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	6
9	RS1	No connection required	
10	VEER	Receiver Ground (Common with Transmitter Ground)	
11	V _{EER}	Receiver Ground (Common with Transmitter Ground)	
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	VEER	Receiver Ground (Common with Transmitter Ground)	
15	V _{CCR}	Receiver Power Supply	
16	V _{CCT}	Transmitter Power Supply	
17	V	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V _{EET}	Transmitter Ground (Common with Receiver Ground)	1

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Notes:

- Circuit ground is internally isolated from chassis ground.
- 2. T_{FAULT} is an open collector/drain output, which should be pulled up with a 4.7k 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc + 0.3V.A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.</p>
- Laser output disabled on T_{DIS} >2.0V or open, enabled on T_{DIS} <0.8V.
- Should be pulled up with 4.7kΩ- 10kΩ host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.
- Internally pulled down per SFF-8431 Rev 4.1.
- LOS is open collector output. It should be pulled up with 4.7kΩ 10kΩ on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

VI. Digital Diagnostic Functions

CARELINK CL-SFP+LRM_220transceivers support the 2-wire serial communication protocol as defined in the SFP+ MSA. The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, CARELINK SFP+ transceivers provide a unique enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP+ MSA defines a 256-byte memory map in EEPROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged.

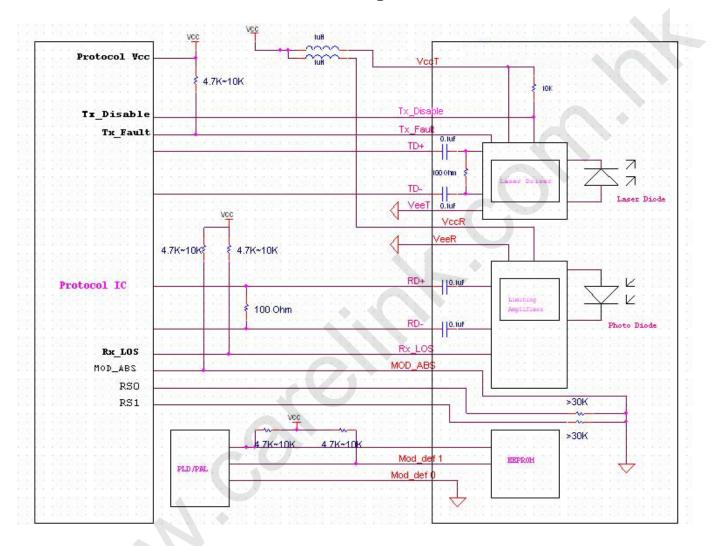
The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver

into those segments of the E2PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data word that can be addressed individually or sequentially.

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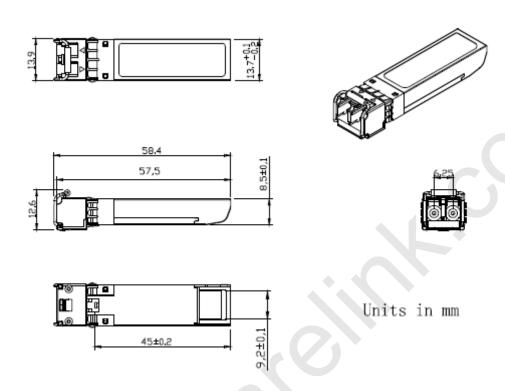
VII. Host - Transceiver Interface Block Diagram





VIII. Outline Dimensions

Comply to SFF-8432 rev. 5.0, the improved Pluggable form factor specification.



IX. Regulatory Compliance

Feature	Reference	Performance		
Electrostatic discharge (ESD)	IEC/EN 61000-4-2	Compatible with standards		
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN 55022 Class B (CISPR 22A)	Compatible with standards		
Laser Eye Safety	FDA 21CFR 1040.10, 1040.11 IEC/EN 60825-1,2	Class 1 laser product		
Component Recognition	IEC/EN 60950, UL	Compatible with standards		
ROHS	2002/95/EC	Compatible with standards		
EMC	EN61000-3	Compatible with standards		

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